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**Document(s) Attached: Restriction Requirement
Total Pages: 2 (including this page)**

In re application of:	Michael P. Belyansky et al.
Serial Number:	10/605,889
Filing Date:	November 4, 2003
Attorney:	Joseph P. Abate IBM Corporation Dept.18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

PLEASE DELIVER TO:

EXAMINER **Long Pham**
ART UNIT: **2814**
PHONE NO: 571-272-1714
FAX NO: 703-872-9306

FROM: Daryl K. Neff
 4 Eugene Circle
 Lincoln Park, NJ 07035
 Phone 973-316-2612

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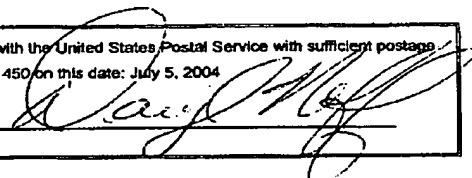
JUL 05 2004

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Name of Depositor:

Daryl K. Neff

Signature & Date



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Michael P. Belyansky, et al.	Date: July 5, 2004
Serial Number: 10/605,889	Examiner: Long Pham
Filed: November 4, 2003	Group Art Unit: 2814
Title: OXIDATION METHOD FOR ALTERING A FILM STRUCTURE AND CMOS TRANSISTOR STRUCTURE FORMED THEREWITH	IBM Corporation D/18G, B/300, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

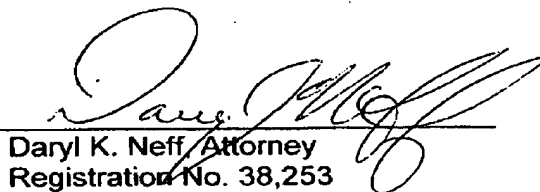
In response to the Restriction Requirement mailed June 7, 2004 restricting examination to Group I (Claims 1-20), drawn to a process of making a semiconductor device; and Group II (Claims 21-30) drawn to a semiconductor device, Applicants elect the claims of Group I (Claims 1-20) without traverse.

Prompt examination of this application is respectfully requested.

Respectfully submitted,

Michael P. Belyansky, et al.

By:



Daryl K. Neff, Attorney
Registration No. 38,253
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